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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known

Application Number	09/992,130
Filing Date	11/14/2001
First Named Inventor	RONALD HILTON
Art Unit	2128
Examiner Name	SAXENA, AKASH
Attorney Docket Number	AMDH-08152US0 DEL

Sheet	1	of	3
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U. S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

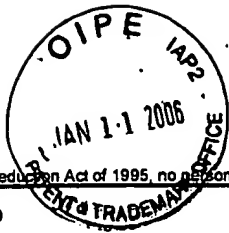
FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code* Number* Kind Code* (if known)				
ML	6	G.B. No. 2,203,572	10/19/1998	MACGREGOR, R.	FIG 1,4,5,6,7,11,11	

Examiner Signature	<u>Heath Lewis</u>	Date Considered	5/12/06
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PTO/SB/088 (07-05)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Application Number	09/992,130
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First Named Inventor	RONALD HILTON
Art Unit	2128
Examiner Name	SAXENA, AKASH
Attorney Docket Number	AMDH-08152US0 DEL

Sheet 2 of 3

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
Be	7	APIKI, S., "Windows on RISC," Byte, April 1994, pp. 109-115.	
	8	BERGH, A., KIELMAN, K., MAGENHEIMER, D., and MILLER, J., "HP3000 Emulation on HP Precision Architecture Computers," Hewlett-Packard Journal, December 1987, pp. 87-89.	
	9	BOYD-MERRITT, R., "IBM, NCR Show Parallel Mainframes," Electronic Engineering Times, November 15, 1993, pp. 16,138.	
	10	DE MAINE, P., and LEONG, S., "Transportation of Programs," Abstract: "Proceedings of the Fifteenth Southeastern Symposium on System Theory, March 1983, pp. 158-164."	
	11	HALFHILL, T., "Emulation: RISC's Secret Weapon," Byte, April 1994, pp. 119-130.	
	12	HALFHILL, T., "80x86 Wars," Byte, June 1994, pp. 74-88.	
	13	MAY, C., "MIMIC: A Fast System/370 Simulator," SIGPLAN '87 Symposium on Interpreters and Interpretive Techniques, June 1987, pp. 1-13.	
	14	POOLE, L., "PowerPC Preview," Macworld, February 1994, pp. 102- 108.	
Be	15	SLATER, M., "Windows NT: Putting the Squeeze on Intel," OEM Magazine, July 1993, pp. 52-57,90.	

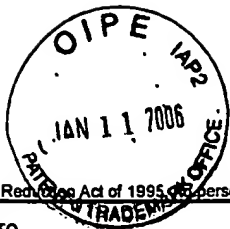
Examiner Signature	<u>Akash Saxena</u>	Date Considered	<u>5/12/02</u>
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Sheet 3 of 3

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
He	16	THELEN, R., "The Power Mac's Run-Time Architecture," Byte, April 1994, pp. 131-138.	
He	17	THOMPSON, T., and RYAN, R., "Apple, IBM Bring PowerPC to the Desktop," Byte, April 1994, pp. 44-72.	
	18	TIMMINS, L., "Techniques for Translating Object Code into a Reduced Microprocessor Architecture," Proceedings of IESMM International Symposium on Software and Hardware Applications of Microcomputers, February 1986, pp. 181-184.	
		not legible	

Examiner Signature	Akash Saxena	Date Considered	5/12/06
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